Verification of system-wide safety properties of ROS applications

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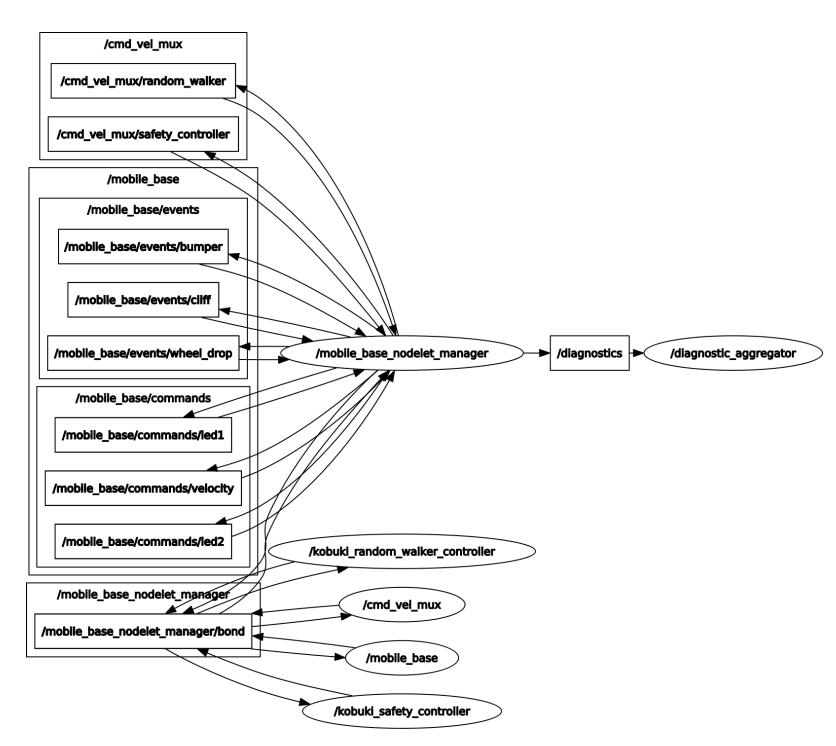
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Verifying system-wide safety properties A toy example



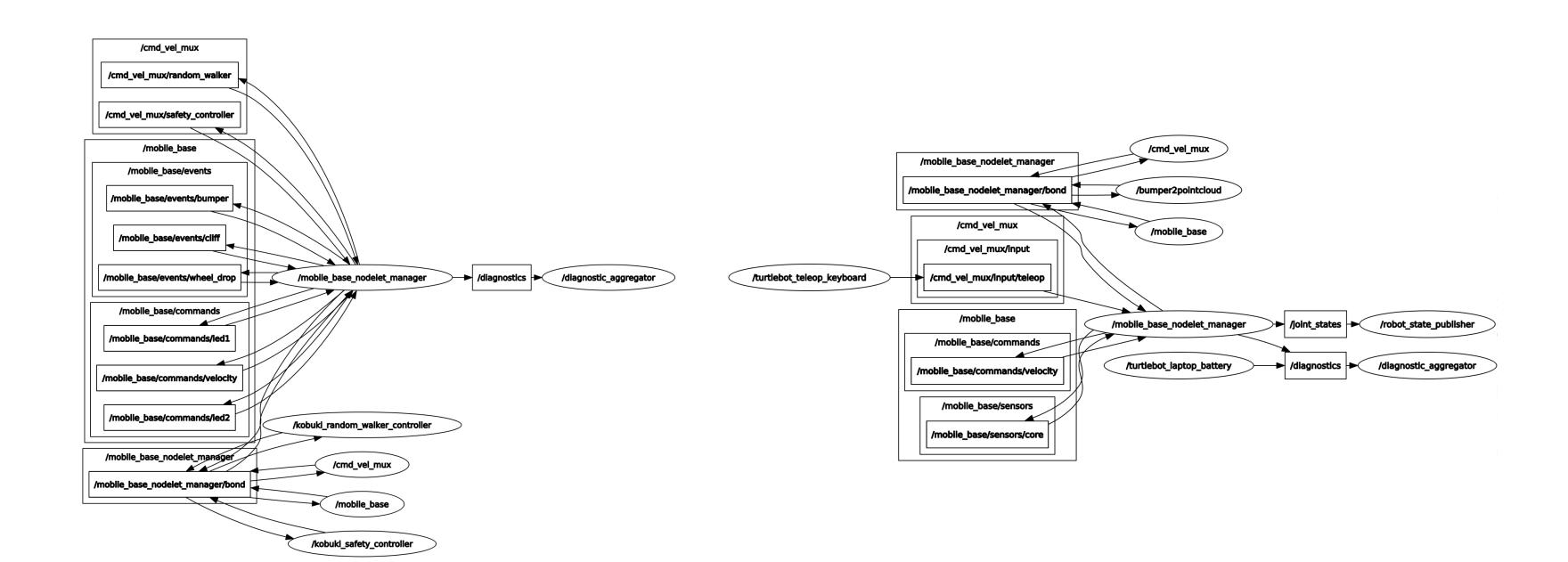
A toy example





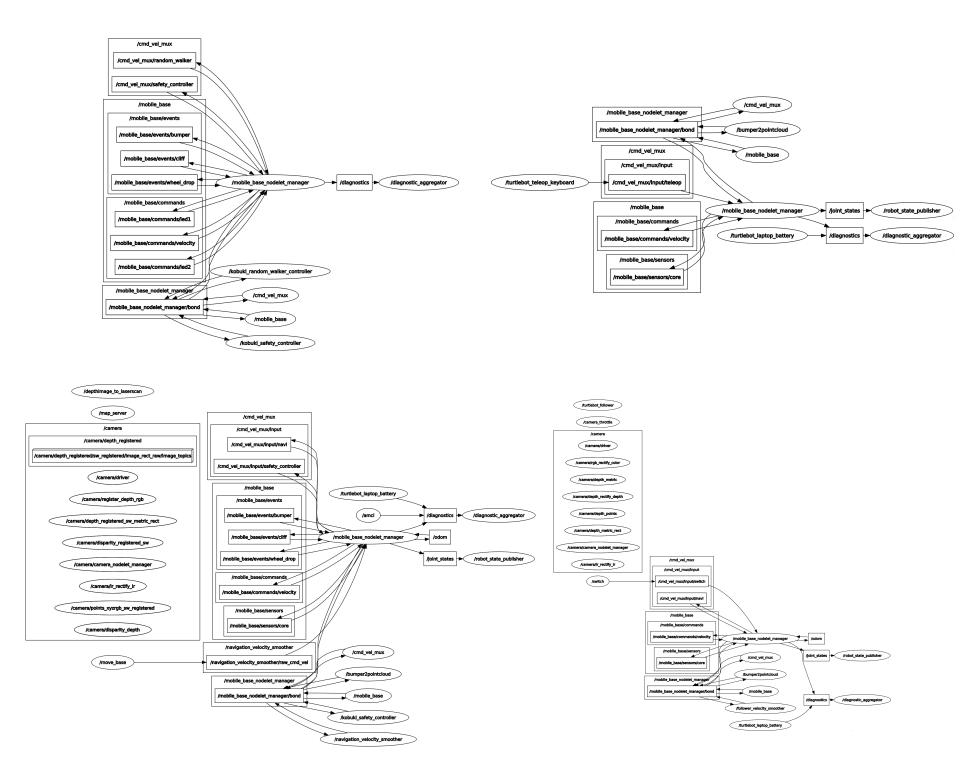
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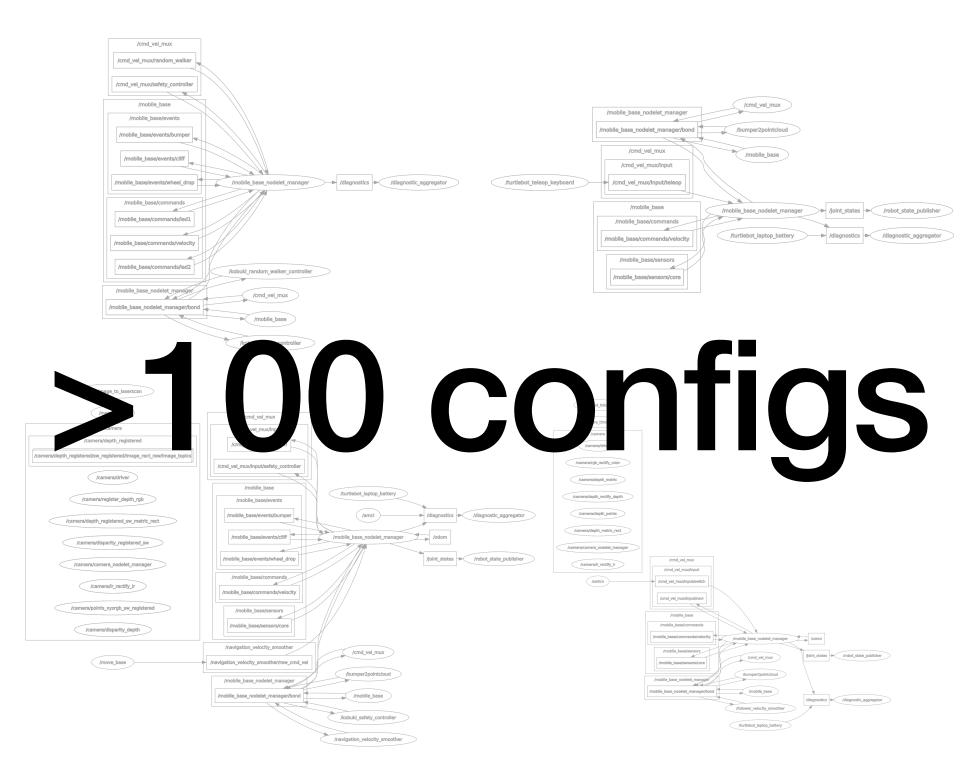
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A toy example





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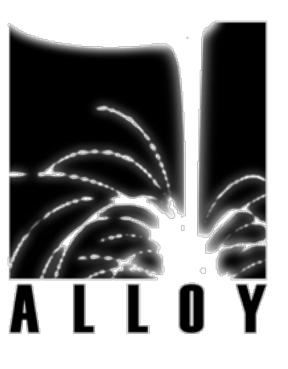


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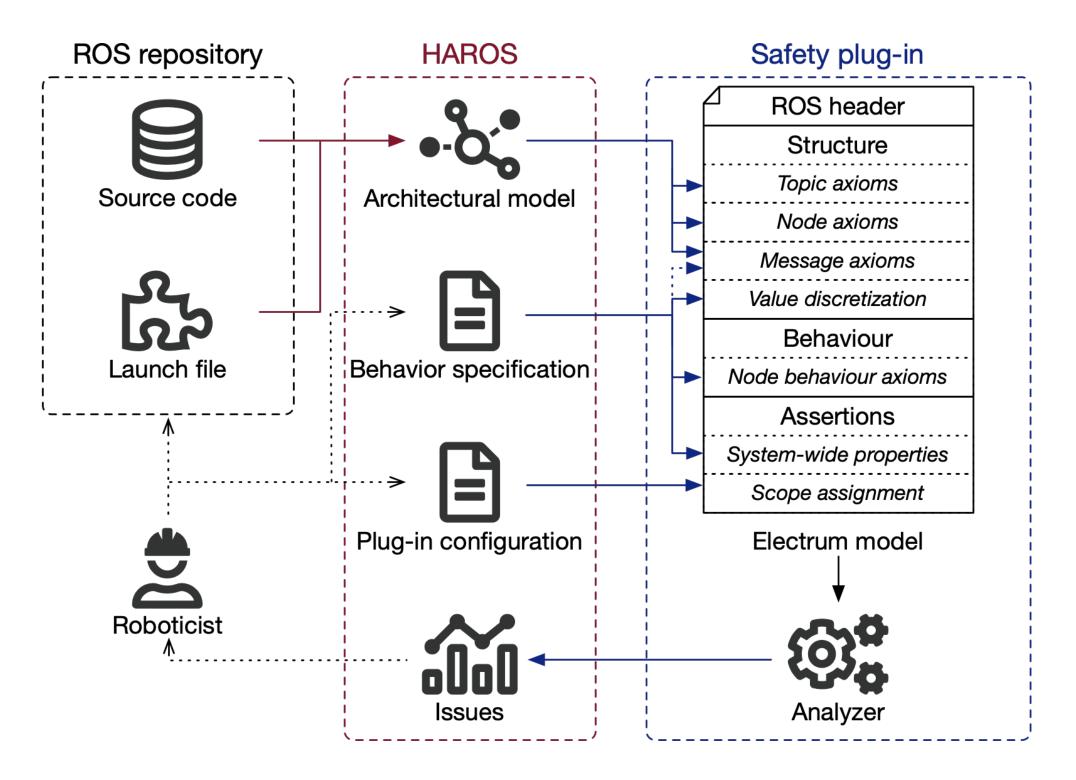




Verifying system-wide safety properties Our proposal

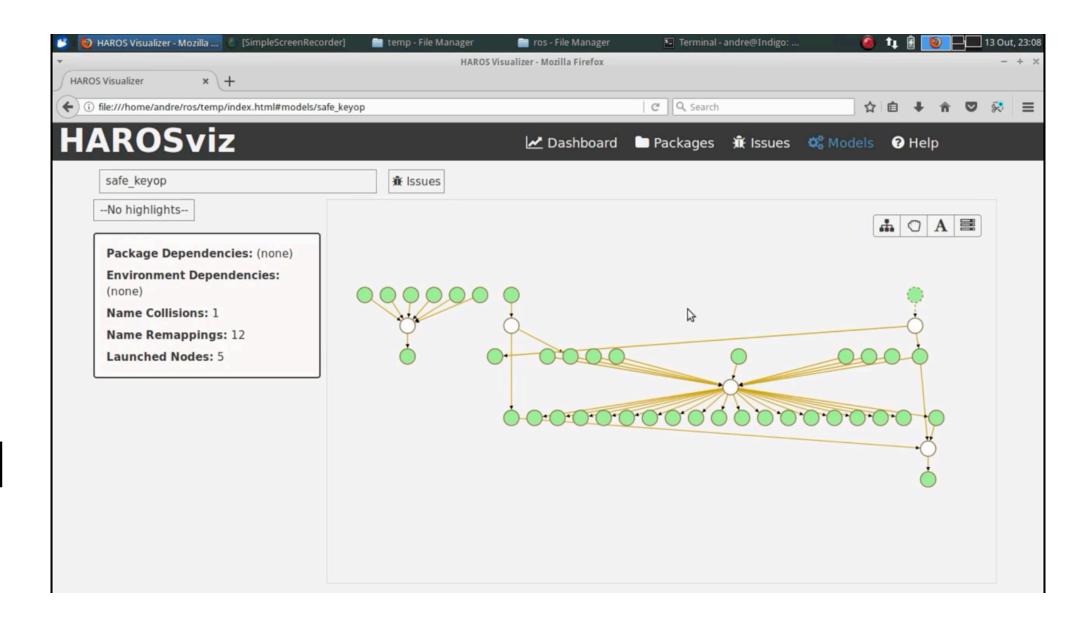
A model checking plug-in for HAROS

- User only required to loosely specify node behaviour and desired properties
- Automatically reports counter-examples in a ROS-friendly interface



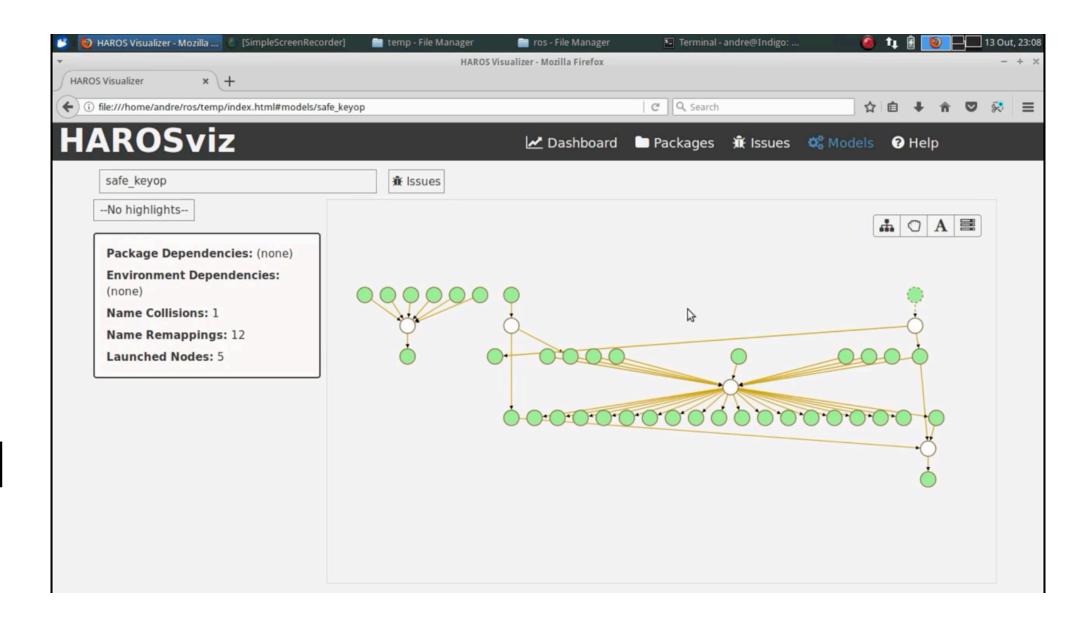
The quality assurance platform

- Plug-in based platform for ROS quality assessment in continuous integration
- Automatically extracts arquitectural models from ROS code
- Detected issues are presented in a unified interface, with traceability to code



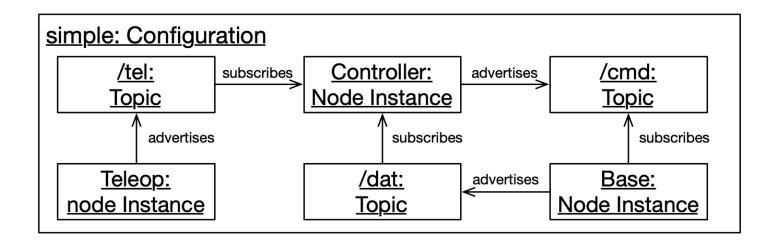
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The specification language

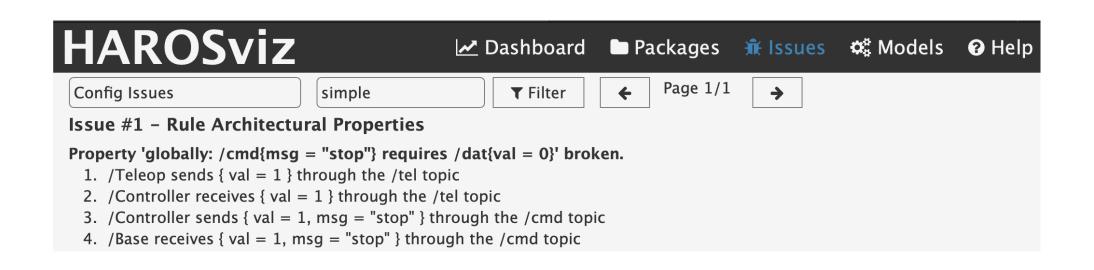
- HAROS provides a DSL to specify behaviours as well-known patterns
- A specification has an activation scope and a property on message events
- Can be used to specify the behaviour of individual nodes and of the whole system

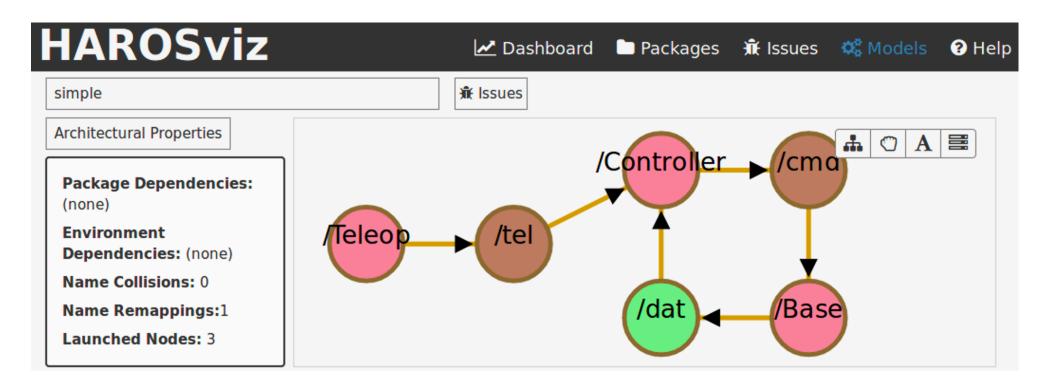


```
Teleop:
    globally: no /tel{val not in 0 to 100}
Controller:
    after /dat{val=0} until /dat{val!=1}:
        no /cmd{val!=0}
    ...
    globally: /cmd{val!=0} as m requires /tel{val=$m.val}
    globally: /dat{val=0} causes /cmd{val=0, msg="stop"}
simple:
    globally: /cmd{msg="stop"} requires /dat{val=0}
```

The model checking plug-in

- Translates a ROS architecture, node behaviour and desirable properties into Alloy
- Reports back counter-examples at the ROS-level, either textually or graphically at the architecture

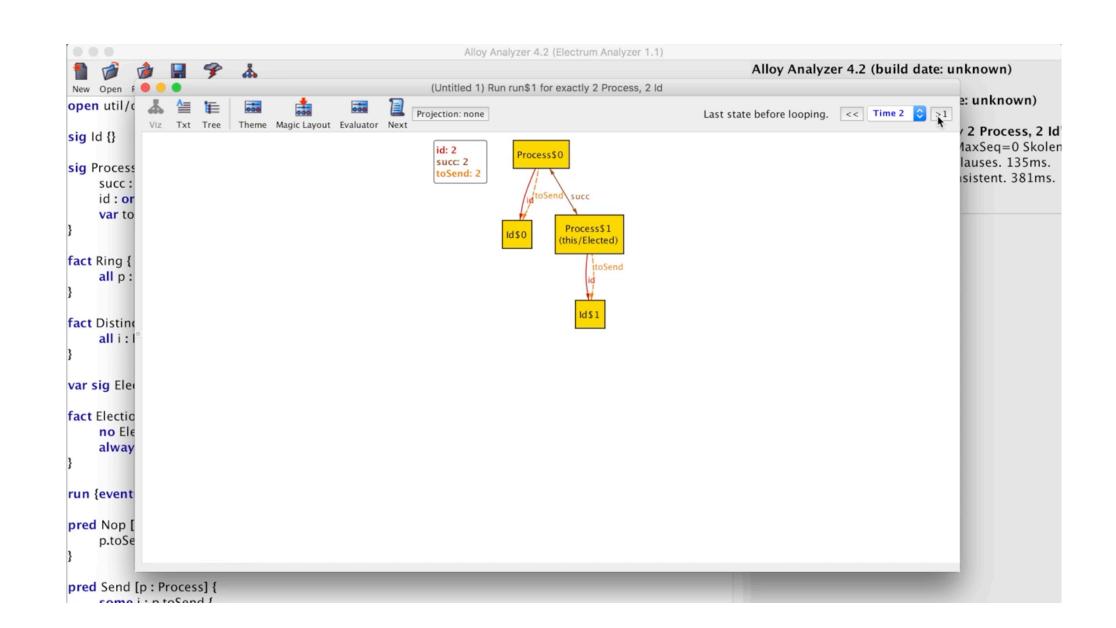




Alloy6 (Electrum)

A model checker for relational linear temporal logic

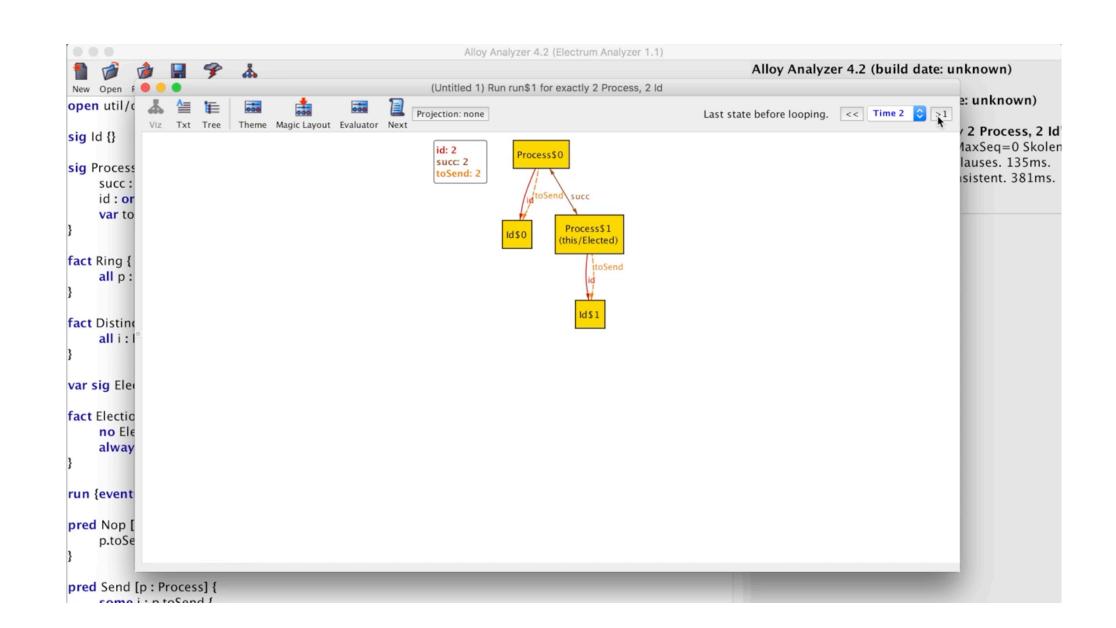
- Formal specification language with structural and dynamic constructs
- Declarative specifications, behaviour can be under-specified
- Automatic verification through SAT solving, returns counter-example traces
- Varying universe size provides increasing confidence



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Alloy6 (Electrum) Encoding ROS architectures

- Architectures encoded in a straightforward way as fixed signatures
- Messages (and their values) are left unrestricted
 - An arbitrary number will be considered (within scopes)
- Large types (eg, integers) would overwhelm analysis
 - Relevant ranges inferred from the specs, only those classes are encoded

```
abstract sig Topic, Field, Value {}
sig IntVal, StrVal extends Value {}
abstract sig Node { subs, advs
                                     : set Topic,
                    var inbox, outbox: set Message }
sig Message { topic: one Topic,
              val : Field->lone Value }
one sig tel, dat, cmd extends Topic {}
one sig Teleop, Base, Controller extends Node {}
fact Links {
  advs = Teleop->tel + Base->dat + Controller->cmd
  subs = Controller->(tel+dat) + Base->cmd }
one sig tel val, dat val, cmd val, cmd msg extends Field {}
fact Fields {
  all m: topic.cmd {
   m.val in cmd val->IntVal + cmd msg->StrVal
   m.val in (cmd val+cmd msg) ->one (IntVal+StrVal) }
lone sig Int_0,Int_1 in IntVal {}
sig Int 0 10, Int 0 100 in IntVal {}
fact Values
  Int_0+Int_1 in Int_0_10 and Int_0_10 in Int_0_100
 no Int 0&Int 1 }
```

Alloy6 (Electrum)

Encoding ROS behaviours

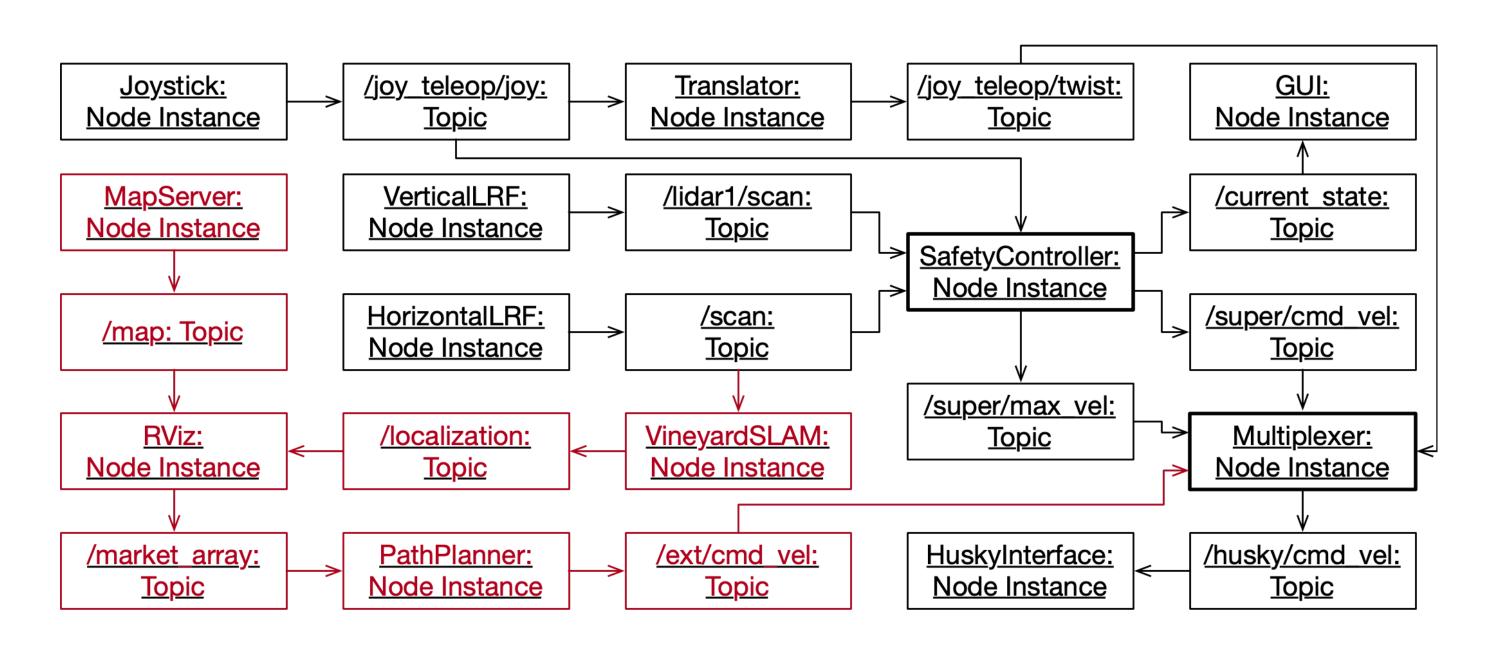
- The message-passing behaviour is loosely specified
 - Messages in the outbox will eventually reach the inboxes of subscribers
- Semantics of the specification language is provided in (metric) linear temporal logic
- Node behaviour imposed as a fact, desirable system properties as assertions to be checked

```
fact Messages { always
 all m: Node.outbox {
   all n: subs.(m.topic) | eventually m in n.inbox
   eventually m not in Node.outbox }
 all m: Node.inbox
   before once m in advs.(m.topic).outbox
fact NodeBehavior { always
 no m: Teleop.outbox&topic.tel |
   tel val. (m.val) not in Int 0 100
 all m: Controller.inbox&topic.dat
   dat val.(m.val) = Int 0 implies after eventually
      some m0: Controller.outbox&topic.cmd {
        cmd val.(m0.val) = Int 0
        cmd msg.(m0.val) = Str stop \} \}
assert simple1 { always {
 all m: Node.outbox&topic.cmd
   cmd msg.(m.val) = Str stop implies before once
      some m0: Node.outbox&topic.dat |
       dat val.(m0.val) = Int 0 }
```

Evaluation

Case study: AgRob V16





Modular robot, alternative configurations for navigation

Evaluation

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```
SafetyController:
  globally: /current_state{data[0]=6} requires /joy_teleop/joy{button[0]=1}
  globally: no /super/cmd_vel{linear.x not in 0 to 10}
  globally: /super/cmd vel{linear.x in 3.8 to 4.2} requires
                /joy teleop/joy{button[0]=0, button[1]=1} | |
                /joy teleop/joy{button[4]=1, button[5]=0}
map:
  globally: /agrobv16/current state{data[0]=3} requires
                /joy_teleop/joy{button[0]=0, button[1]=1}
  globally: /husky/cmd_vel{linear.x=0, angular.x in -100 to 100} requires
                /scan{ranges[0] in 0 to 4} ||
                /joy teleop/joy{button[0]=1}
```

Modular robot, alternative configurations for navigation

Evaluation

Case study: AgRob V16

- Finds a counter-example caused by change in architecture
 - Wrong specification
 - Less than 1 minute for 10 Messages

[Ver se tenho a VM a funcionar e sacar screenshow]

Limitations and future work

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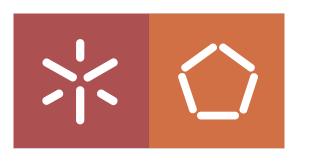
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 - Further abstractions could address certain timed issues
- Loose specified behaviour may lead to false positives
 - Explore the combination of static and runtime analysis

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